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FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of	1

	A PETAPIO 10 DEC 2001
Com	APCT/PTO 10 DEC 2004
Application Number	Unassigned 71/495
Filing Date	December 10, 2004
First Named Inventor	Hirofumi Totsuka et al.
Examiner Name	Unassigned
Attorney Docket Number	032404-082

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NON-PATENT LITERATURE DOCUMENTS						
Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
/J.P./	*ANSGAR POTTBACKER ET AL., "A Si Bipolar Phase and Frequency Detector IC for Clock Extraction up to 8GB/s", IEEE Journal of Solid State Circuits", December 1992, Vol. SC-27, pp. 1747-1751.					

*Copy Attached

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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December 10, 2004	AUG 1 3 2007 B
Hirofumi Totsuka et a	12 6
JAYANTI K PATEL	(A)
1032404-000082	THADENA
	10/517,493 December 10, 2004 Hirofumi Totsuka et a JAYANTI K PATEL

Sheet 1	Sheet 1 of 1								
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NON-PATENT LITERATURE DOCUMENTS								
Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.								

Examiner Signature	/Jason Perilla/	Date Considered	10/30/2007	
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